

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

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1. (Currently Amended) A method for controlling a data read/write operation comprising:

a data receiving step of receiving predetermined data to be written to a disk from an upper-level host system;

a data processing step of conducting predetermined processing on said received data; and

a data write-in step of writing said processed data to said disk,

wherein said data processing step comprises:

a data dividing step of dividing said data received at said data receiving step into a plurality of data items and also generating parity data;

a data storing step of individually storing said divided data items and said parity data items into respective cache modules respectively;

a data repairing step of fetching said divided data items and said parity data from said cache modules and repairing one of said divided data items, if said one is damaged, using said parity data and only the remaining ones of said divided data items; and

a data combining step of combining said divided data items.

2. (Currently Amended) A method for controlling a data read/write operation comprising:

a data read-out step of reading out predetermined data to be transmitted to an upper-level host system from a disk;

a data processing step of conducting predetermined processing on said read out data; and

a data transmitting step of transmitting said processed data to said upper-level host system,

wherein said data processing step comprises:

a data dividing step of dividing said data read out at said data read-out step into a plurality of data items and also generating parity data;

a data storing step of individually storing said divided data items and said parity data items into respective cache modules respectively;

a data repairing step of fetching said divided data items and said parity data from said cache modules and also repairing one of said divided data items, if said one is damaged, using said parity data and only the remaining ones of said divided data items; and

a data combining step of combining said divided data.

3. (Currently Amended) A disk array apparatus comprising an array controlling unit for receiving an instruction from an upper-level host system to thereby write predetermined data to or read said predetermined data out from a disk and also conduct operational processing on said predetermined data, wherein said array controlling unit comprises:

a data dividing function for dividing said predetermined data into at least two data items and also generating parity data for said predetermined data; and

a data combining function for repairing one of said divided data items, if said one is damaged, using said parity data and only the remaining ones of said divided data items, and also combining said divided data items.

4. (Currently Amended) A disk array apparatus comprising an array controlling unit for receiving an instruction from an upper-level host system to thereby write predetermined data to or reading said predetermined data from a disk and also conduct operational processing on said predetermined data, wherein said array controlling unit comprises:

a data dividing section for dividing said predetermined data into at least two data items and also generating parity data based on said predetermined data;

a plurality of cache modules for temporarily storing said divided data items and said parity data respectively; and

a data combining section for repairing ~~said divided data item~~ one of said divided data items stored in one of said cache modules, if said one cache module fails, using only the remaining ones of said divided data items and said parity data and also combining said divided data items.

5. (Original) The disk array apparatus according to Claim 4, wherein said cache modules are set to have an equal capacity.

6. (Currently Amended) The disk array apparatus according to Claim 4, wherein each of said divided data items and said parity data are set to have an equal length capacity.

7. (Previously Presented) The disk array apparatus according to Claim 4, wherein a total number of said divided data items and said parity data items is set equal to a number of said cache modules.

8. (Original) The disk array apparatus according to Claim 4, wherein a number of said divided data items is set one smaller than a number of a number of said cache modules.

9. (Currently Amended) A recording medium for recording a data read/write controlling program, said program comprising:

a<sub>2</sub> a data dividing process for receiving predetermined data to be written to a disk from an upper-level host system to then divide said predetermined data into a plurality of data items and also generate parity data;

a data storing process for individually storing said divided data items and said parity data into respective cache modules respectively;

a data repairing process for fetching said divided data items and said parity data from said cache modules to thereby repair one of said divided ~~data item~~ data items, if said one is damaged, using said parity data and only the remaining ones of said divided data items; and

a data combining process of combining said divided data items to then write this combined data to said disk.

10. (Previously Presented) The disk array apparatus according to Claim 5, wherein a total number of said divided data items and said parity data items is set equal to a number of said cache modules.

11. (Previously Presented) The disk array apparatus according to Claim 6, wherein a total number of said divided data items and said parity data items is set equal to a number of said cache modules.

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